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24309 7590 10/28/2003

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Pat Slaback (Depositor's name)
Pat Slaback (Signature)
January 28, 2004 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/090,239	03/01/2002	Andi V. Ghia	X-1061 US	6539

TITLE OF INVENTION: LOW JITTER CLOCK FOR A PHYSICAL MEDIA ACCESS SUBLAYER ON A FIELD PROGRAMMABLE GATE ARRAY

APPLN. TYPE	SMALL ENTITY	ISSUE FES	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	01/28/2004
EXAMINER	ART UNIT	CLASS-SUBCLASS			
TRAN, ANHQ	2819	326-041000			

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E. Eric Hoffman
2
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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

XILINX, INC.

San Jose, California 95124

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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- ☒ Issue Fee
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1/28/04

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OMB 0651-0033

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Inventor(s): Atul V. Ghia et al.

Assignee: **XILINX, INC.**

Serial No.: 10/090,239

conf.no. 6539

Filed: March 1, 2002

Title: Low Jitter Clock for a Physical Media Access Sublayer on a Field Programmable Gate Array

Docket No.: **X-1061 US**

Enclosed: Return Receipt Postcard
Fee Transmittal - Part B

Date: January 28, 2004

VIA FACSIMILE 703-746-4000

Atty/Sec: KK/pbs